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Spartan-6 FPGA Family - Xilinx - All Programmable

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Xilinx ISE - Wikipedia

Spartan-6 Family Overview DS160 (v2.0) October 25, 2011 www.xilinx.com Product Specification 2
Spartan-6 FPGA Feature Summary Table 1:Spartan-6 FPGA Feature Summary by Device Device Logic

Xilinx DS160 Spartan-6 Family Overview

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Digital Design and Computer Architecture: David Harris

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Field-programmable gate array - Wikipedia

This page contains the complete set of materials for my FPGA & Verilog design course which I taught in Isfahan University of Technology, 2010.

FPGA & Verilog Design â€“ Mohammad S. Sadri - Googoolia

Aldec, Inc. offers a mixed-language simulator with advance debugging tools for ASIC and FPGA designers. It also includes text, finite state machine and schematic editor and design documentation tools, fpga simulation, vhdl simulation, fpga simulator.

Active-HDL - FPGA Simulation - Products - Aldec

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